

App. No. 09/245,292  
Amdt dated July 16, 2004  
Reply to telephone interview of June 2004

a processor system coupled to the first and the second interfaces, the processor system controlling a flow of message traffic to and from the first and the second interfaces; and

a single housing containing the first and the second interfaces and the processor system.

Claim 47 (currently amended) A method for controlling communications in a multi-protocol wireless network, comprising:

receiving first digital communications according to a first protocol at a first interface in a common housing;

sending a first control message according to the first protocol;

receiving second digital communications according to a second protocol at a second interface in the common housing;

receiving intrasystem communications at a intrasystem message handler;

receiving intersystem communications at a intersystem message handler; and

sending a second control message according to the second protocol, wherein a processor in a switching center interprets the first and the second digital communications and generates the first and the second control messages, and wherein the switching center is located in the common housing.

Claim 48 (cancelled).

*49*  
Claim 48 (currently amended): The method of claim 48<sup>49</sup>, wherein the intrasystem message handler operates according to IS-634 and GSM standards and the intersystem message handler operates according to IS-41 and GSM standards.

Claim 50 (original). The method of claim 49, wherein the GSM protocols include GSM A protocols, IS-651 protocols, IS-652 protocols and GSM 09.02 protocols.

Claim 51 (original): The method of claim 49, wherein the IS-634 and IS-41 protocols include time division multiple access (TDMA) protocols and code division multiple access (CDMA) protocols and AMP protocols.